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PATENT
Attorney Docket No.: SSI-00501

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Maximilian A. Biberger et al.

Serial No.: 09/912,844

Filed: July 24, 2001

For: **HIGH PRESSURE PROCESSING
CHAMBER FOR
SEMICONDUCTOR SUBSTRATE**

Group Art Unit: 1763

Examiner: Ram N. Kackar

TRANSMITTAL LETTER

162 N. Wolfe Road
Sunnyvale, CA 94086
(408) 530-9700

HW
6/13/03
ML

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

Sir:

Enclosed please find a Supplemental Information Disclosure Statement, a Certification pursuant to 37 CFR § 1.97(e) for Information Disclosure Statement, and Form PTO-1449, including a copy of the reference contained thereon, for filing in the U.S. Patent and Trademark Office.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

Respectfully submitted,

HAVERSTOCK & OWENS LLP

Dated: 5/23/03

By: Thomas B. Haverstock
Thomas B. Haverstock
Reg. No.: 32,571

Attorneys for Applicants

Adjustment date: 08/18/2003 EEKUBAY1
06/03/2003 HWAGSTAF 00000001 081275 09912844
01 FC:1806 180.00 CR

06/03/2003 HWAGSTAF 00000001 081275 09912844
01 FC:1806 180.00 CR

CERTIFICATE OF MAILING (37 CFR § 1.8(a))
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HAVERSTOCK & OWENS LLP
Date: 5/27/03 By: [Signature]



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Maximilian A. Biberger et al.)	Examiner: Ram N. Kackar
Serial No.: 09/912,844)	<u>CERTIFICATION FOR</u>
Filed: July 24, 2001)	<u>SUPPLEMENTAL INFORMATION</u>
For: HIGH PRESSURE PROCESSING)	<u>DISCLOSURE STATEMENT UNDER 37</u>
CHAMBER FOR)	<u>CFR § 1.97(e)</u>
SEMICONDUCTOR SUBSTRATE)	162 North Wolfe Road
)	Sunnyvale, CA 94086
)	(408) 530-9700

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This certification is being made for the Supplemental Information Disclosure Statement accompanying this certification.

Certification

I, the person signing below, hereby certify that the printed publications contained in the enclosed supplemental information disclosure statement were cited within an Official Action mailed on February 28, 2003. **Accordingly, I hereby certify that no reference contained within this supplemental information disclosure statement was cited in a communication from a foreign patent office in a related foreign application, and, to the knowledge of the person signing the statement, after making reasonable inquiry, no reference contained within this supplemental information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of this supplemental information disclosure statement. 37 C.F.R. 1.97(e)(2)**

Attorney Docket No.: PATENT
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Identification of Person Making Certification

The person making this certification is the attorney who signs below on the basis of the information in the attorney's file.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: 5-23-03

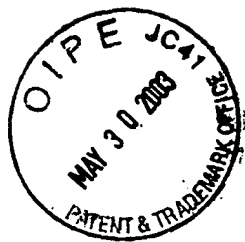
By: Thomas B. Haverstock
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Reg. No.: 32,571

Attorneys for Applicants

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SEMICONDUCTOR SUBSTRATE)	(408) 530-9700

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P.O. Box 1450
Alexandria, VA 22313

Sir:

The citation listed below, copy attached, may be material to the examination of the above-identified application, and is therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make this citation of official record in this application.

Applicant has become aware of the following printed publication which may be material to the examination of this application:

- U.S. Patent No. 6,319,858 B1

This Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: 5-23-03

By:



Thomas B. Haverstock
Reg. No.: 32,571

Attorneys for Applicants

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